

Notice of Allowability

Application No.

10/766,192

Examiner

MANSOUR M. SAID

Applicant(s)

TERAISHI, TOSHIO

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 1/29/04.
2. ☒ The allowed claim(s) is/are 1-15.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 1/29/04
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600

DETAILED ACTION

Allowable Subject Matter

1. Claims 1-15 are allowed.

The following is an examiner's statement of reasons for allowance: Claims 1-15 are allowed since certain key features of the claimed invention are not taught or fairly suggested by prior art. **In claim 1**, “fourth shorting means for shorting a first voltage higher than the predetermined potential, among the plurality of the voltages generated by the gradation voltage generation circuit, and a second voltage lower than the predetermined potential, among the plurality of the voltages generated by the gradation voltage generation circuit, with the odd numbered columns of the source lines and the even numbered columns of the source lines, respectively, after switching over between the first voltage and second voltages in a predetermined cycle. In claim 7, “wherein the source lines are shorted to a desired voltage selected from among the voltages generated by the gradation voltage generation circuit, and subsequently, the polarity of voltages of the source lines, at the predetermined potential, is enabled to shift to the same polarity as that of the desired voltage selected”. **In claim 11**, “causing desired voltages generated by the gradation voltage generation circuit to be shorted to the source lines after shorting the source lines with each other”. The closest prior art Kodama Hidesato et al. (JP 11-030975) teach a drive circuit of liquid crystal display having a switching element and liquid crystal capacity at each intersection of two or more gate lines and two or more source lines ... the source line mechanical-component output from the source line at the time of the early stages of writing to liquid crystal capacity, Tamai et al. (5,828,357) teaches a

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potential corresponding to gradation display data is supplied to the pixel electrodes via source lines to perform gradation display in accordance with the gradation display data, however, singularly or in combination with other prior art, fail to anticipate or render the above underlined limitations obvious.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Matsuda et al. (5,886,679) teach a driver circuit for driving liquid crystal display includes a driving voltage selector circuit including positive and negative driving voltage selector circuits.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mansour M. Said whose telephone number is 571-272-7679. The examiner can normally be reached on Monday through Thursday from 8:30-6:00 P.M. The examiner can also be reached on alternate Friday from 8:30 a.m. to 5:00 p.m. EST. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe whose telephone number is 571-272-7681.

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Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231


or faxed to: 571-273-8300 (for Technology Center 2600 only)

Hand-delivered responses should be brought to the Customer Service Window at the Randolph Building, 401, Dulany Street, Alexandria, VA 22314.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mansour M. Said

9/27/06



RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
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